

## MICHALA DENNIS

michalajd@gmail.com | linkedin.com/in/michala-dennis-281637205  
871 Concorde Circle Apt. #20204 Linthicum Heights, MD 21090; (570) 855-6797

### EDUCATION

**Lafayette College**, Easton, PA

B.S. in Electrical and Computer Engineering, Minor in Music

**Graduated May 19, 2024**

**GPA: 3.62**

**George Washington University**, Washington, DC

M.S. Computer Engineering with a concentration in Computer Architecture & High-Performance Computing.

**Expected: May 2026**

**GPA: n/a**

### ACADEMIC HONORS

Honors Thesis in Electrical and Computer Engineering: defended Spring 2024

Dean's List: Fall 2020, Fall 2021, Spring 2023, Fall 2023, Spring 2024

Alpha Psi Omega (National Theatre Honors Society): 2023-present (Treasurer/Business Manager)

Jacobs National Internship Presentation Competition, National Finalist: August 2023

IEEE Eta Kappa Nu (Electrical & Computer Engineering Honors Society): 2024-present (Member)

### SKILLS

**Computer:** Microsoft Office Suite, Java, SystemVerilog, GitHub, C (Embedded Systems), C++, Matlab, Python, R

**Language:** Can read and write in Spanish and can comprehend Spanish speaking

### EXPERIENCE

**Jacobs**, Computer Engineering Intern (Severn, MD)

**August 2023-January 2024**

Works on a team with engineers that program a simulator for a space-based ISR subsystem. Uses linting tools like ClangTidy to clean code for easier readability. Develops new features in C++ and Python to improve the functionality of the subsystem. Works with advanced software such as Jenkins and Docker, and works with Atlassian programs like Jira and Bitbucket.

**Lafayette ECE Department Research**, Research Assistant (Easton, PA)

**June 2021-May 2024**

Assists professor John A. Nestor in developing different types of priority queues in hardware using SystemVerilog and FPGAs. Analyzes components such as number of look-up tables and maximum timing delay for various designs. Builds various priority queue systems using a SystemVerilog package as a guide.

**Lafayette College Admissions**, Student Director of Connection (Easton, PA)

**September 2021-May 2024**

Connects prospective students with current students through email to help them learn about Lafayette College and make their admissions decision. Manages and analyzes data through the Google Suite for applicants. Creates admissions graphics for social media and works with full-time directors on office administration tasks.

### ACTIVITIES

**IEEE- Institute of Electrical and Electronics Engineers**, Vice Chair

**2020-2024**

**Engineering Student Council**- Engineering peer mentor

**2021-2024**

**Lafayette Music Media Committee**- Founder/President

**2023-2024**

### RELEVANT COURSES

**Data Structures and Algorithms**, Fall 2021

Developed object oriented approaches to the design and implementation of software systems. Learned to analyze problems, algorithms and develop object-oriented solutions to problems. Learned to use multiple data structures and the accompanying algorithms to store, index and retrieve data.

**Embedded Systems**, Fall 2022

Designs stand-alone digital systems utilizing embedded microcontrollers. Topics include microcontroller architecture, peripheral functionality and utilization, performance and power consumption, hardware interfacing, interrupts, and real-time operating systems.

**Digital Circuits II**, Fall 2022

Focuses on the implementation of more complex digital systems with a particular emphasis on computer systems, including instruction set architecture, programming in assembly language and C, implementation of computer processors, and memory hierarchies.